## 操作步骤

1、以 GF90F0320 为例,打开 Keil 工程后,点击打开 GF90F0320T.h 头文件,并将定义的"sfr16 DPTR = 0x82"屏蔽。

A Main	15	sfr ACC	=	OxEO;
	16	sfr B	=	OxF0;
an.c	17	sfr SP	=	0x81;
🖨 🛄 com.c	18	sfr DPL	=	0x82;
com.h	19	sfr DPH	=	0x83;
define h	20	sfr INSCON	=	0xA2;
	21	//sfrl6 DPTR		= 0x82;
define.h	22			
GF90F0320T.h	23	/* SYS CLOCK	*/	
GF90F0320T.h	24	sfr CLKSWR	=	0x8E;
GE DEFINE.H	25	sfr CLKCON	=	Ox8F;
	26			

2、选中需要嵌入汇编的.C 文件, 然后然后右击选择 Options For File, 在跳出的选项框中将 Generate Assembler SRC File 以及 Assemble SRC File 勾选为黑色。

Path:	.\Main\main.c		
File Type:	C Source file		Include in Target Build
Size:	4675 Bytes		Always Build
last change:	Mon Aug 20 17:09:37 2018		Generate Assembler SRC File
			Assemble SRC File
			Link Publics Only
Code Bank:	Ŧ		
Stop on Exit Code:	Not specified	<u> </u>	
Select Modules to Always Include:			
ustom Arguments:			

3、看下当前工程 Memory Model,如示例工程为 Large,

🔞 Options for Target 'Target gfe004'

Xtal (MHz): 32.0		Use On-chip ROM (0x0-0x3FFF)				
Memory Model:	Large: variables in XDATA		]			
Code Rom Size:			✓ Use On-chip XRAM (0x0-0x2FF)			
Operating system: None		•				
on one odde mer	Start:	Size:	Off-chip Xdata	memory Dam	Start:	Size:
	Eprom Start: Eprom Eprom Eprom	Size:	n − Off-chip Xdata	memory Ram Ram Ram	Start:	Size:
Code Banking	Nory Start: Eprom Eprom Eprom Start: Eprom Start:	Size:	Off-chip Xdata	Ram Ram Ram Ram v type support	Start:	Size:

4、根据不同的编译模式,在 KEIL 安装目录表下的 keil\c51\lib\中选中相应的库文件添加到工程中

C51S.LIB - 没有浮点运算的 Small model C51C.LIB - 没有浮点运算的 Compact model C51L.LIB - 没有浮点运算的 Large model C51FPS.LIB - 带浮点运算的 Small model C51FPC.LIB - 带浮点运算的 Compact model C51FPL.LIB - 带浮点运算的 Large model

5、添加完成后如下图所示:

🖃 🔊 Target gfe004	14 sfr PSW	=
🖨 🤭 Main	15 sfr ACC	=
	16 sfr B	=
and a main.c	17 sfr SP	=
🗄 🛄 com.c	18 sfr DPL	=
C51L.LIB	19 sfr DPH	=
H D Svs	20 sfr INSCON	=
	as II and month	

6、在需要嵌入汇编的地方加入#pragma asm 以及#pragma endasm 两行代码,在两行代码之间写入相应的汇编程序即可。

55 #pragma asm -56 MOV R0, #RAM DATA NUM 57 CLR A 58 D\_IDATA\_LP: 59 MOV @RO,A DJNZ RO, D IDATA LP 60 61 MOV DPTR, #0 MOV R7, #LOW (RAM\_XDATA\_NUM) MOV R6, #HIGH (RAM\_XDATA\_NUM) CLR A 62 63 64 65 66 D\_XDATA\_LP: MOVX @DPTR,A 67 INC 68 DPTR R7, D\_XDATA\_LP 69 DJNZ DJNZ R6, D XDATA LP 70 71 #pragma endasm

说明:当选定的 C 文件嵌入汇编语句后,调试时将不支持在 C 文件设置断点,只能在 C 文件对应的汇编(.src)文件设置断点。